

HALF YEARLY NEWSLETTER

"TELE-ERA 2024-25: REFLECT, REBUILD, RISE"

THE OFFICIAL NEWSLETTER OF ELECTRONICS AND TELECOMMUNICATIONS
PCE



**PRIYADARSHINI COLLEGE
OF ENGINEERING**



**"Let the echoes of our engineering
Resonate not just in machines, but in
the hearts of generations."**



MESSAGE FROM HOD DESK



It gives me great pleasure to present our annual newsletter TELE-ERA, a reflection of the department's vibrant academic and cultural journey. This year has been filled with growth, innovation, and teamwork by both students and faculty. I appreciate the efforts of the forum, coordinators, and contributors in making every event a success. Let us continue to strive for excellence and uphold the spirit of learning and leadership. Wishing everyone continued success in all future endeavors.

~Dr.V.K.Taksande

VISION & MISSION

VISION OF THE DEPARTMENT

To achieve excellence in academics and research by imparting quality education in the rapidly changing fields of Electronics and Telecommunications

MISSION OF THE DEPARTMENT

- To educate the students with the state of art technologies to meet the growing challenges of the industries and educational institutions.
- To equip the students with strong foundations to enable them for continuing education and research.
- Provide ethical and value based education by promoting activities addressing the social needs.

PROGRAM EDUCATIONAL OBJECTIVE

- To provide graduates with a solid foundation in mathematical, scientific and engineering fundamentals required to formulate, analyze & solve complex Engineering problems.
- To create graduates with technical expertise to handle multi-disciplinary projects and with ability to excel in professional career and/or higher education.
- To inculcate leadership qualities among graduates with ethical responsibilities and global security awareness.
- To develop graduates with capability to apply knowledge and evaluate results based on facts, experimentation, research and exploring to new simulation tools.

MESSAGE FROM FORUM INCHARGE



The energy and commitment displayed by our students through the TELE-ERA forum have been remarkable and inspiring. From technical events to cultural and social initiatives, each activity reflected innovation, cooperation, and leadership. I applaud the dedication of every forum member for contributing to such a successful year. Together, let us build on this momentum, encourage new ideas, and make the forum a platform that continues to shape bright and responsible engineers for the future.

~Ms.J.C.Kolte

TELE-ERA CORE-COMMITTEE

Sr.No.	Post	Name of Student
1	President(Boy)	Mr.Aditya Lade
2	President (Girl)	Ms.Akansha Dhepe
3	Vice President (Boy)	Mr.Ritik Yele
4	Vice President (Girl)	Ms.Dipali Rewatkar
5	Chief Executive(Boy)	Mr.Shrinay Mahajan
6	Chief Executive(Girl)	Ms.Sharvari Gulhane
7	Secretory(Boy)	Mr.Aditya Warkar
8	Treasurer	Ms.Sarvashi Ganjare
9	Joint-Secretory	Ms.Mansi Badwaik
10	Joint-Treasurer	Mr.Anshool Kumar

EDITOR



~Mr.B. C. Borkar



~Mr.Ritik Yele

"Innovation begins with
curiosity,
Teamwork turns ideas into
reality,
Every effort today shapes
tomorrow's success,
Together, we build a legacy of
excellence."

~Mr.Bhavesh C. Borkar

STUDENTS ACHIEVEMENTS

Toppers of Third Semester

1	Gopal.P.Deotare	89.14 %
2	Rajnandini.D.Patle	87.28 %
3	Kalyani.P.Derkar	85.42 %

Toppers of Sixth Semester

1	Pratik Akarte	89.00 %
2	Yashshree Sulbhewar	84.33 %
3	Prashant Dekate	83.00 %

Toppers of Fourth Semester

1	Rajnandini.D.Patle	92.30 %
2	Manisha P. Derkar	89.07 %
3	Snehal A. Raut	88.15 %

Toppers of Seventh Semester

1	Ayush B. Narule	92.90 %
2	Mayuri A. Ramtekar	85.27 %
3	Srishti Siriya	83.09 %

Toppers of Fifth Semester

1	Sanskruti V. Dasarwar	79.23 %
2	Sejal D. Pandey	74.92 %
3	Megha R. Moharkar	74.46 %

Toppers of Eighth Semester

1	Ritik Yele	89.42 %
2	Mayuri A. Ramtekkar	87.14 %
3	Sakshi D. Kumbhare	86.28 %
4	Janhavi R. Chawre	86.28 %

CAMPUS PLACEMENT

Sr No.	Name of Student	Name of Company
1	Akansha Dhepe	HCL Tech TTEC MyCaptain Teachnook
2	Mayuri Ramtekkar	Capgemini HCL Tech
3	Punit Atkar	HCL Tech TTEC
4	Nayan Lende	HCL Tech TTEC
5	Ritik Yele	HCL Tech Teachnook
6	Ayush Narule	Prevoyance IT solutions private Limited
7	Mayur Durbule	Prevoyance IT solutions private Limited
8	Tejas Bhandarkar	TTEC L-Cube
9	Chaitanya Jhulme	TTEC
10	Akshadha Jhunghare	Collabrea MyCaptain
11	Sakshi Kumbhare	Teachnook
12	Srishti Siriya	HCL Tech Teachnook
13	Tushar Bramhankar	Teachnook
14	Janvi Chaware	Teachnook
15	Ayush Shirbhate	Solar Industries Private Limited
16	Aniket Marshettiwar	TTEC Teachnook
17	Aniket Patil	Colosus Infra Project
18	Dipali Rewatkar	Capgemini

Department of Electronics and Telecommunication

Forum Installation Ceremony “Tele-Era 2024-25”



“Forum Installation Ceremony” was organized under the banner of Tele-Era the student forum of ETC Department. The Chief Guest of the function Dr. S.A. Dhale, Principal, The Guest of Honour Dr. G. M. Asutkar, Vice Principal and Dr. P. R. Rothe, Registrar, Dr. (Mrs.) S. W. Varade, Professor Dean (T&P), Dr. V. K. Taksande, H.O.D of ETC, Dean (Student Affairs) graced the event. The President of Forum were Mr. Aditya Lade and Ms. Akansha Dhepe.



The badges and certificates were distributed among all the Core committee members and Incharges of various committees. Tele Era incharges Dr. (Mrs.). S.P.Washimkar & Ms. J.C.Kolte worked hard for the successful conduction of the program.

Fresher's & Cultural Event- 2K24

Students participated in the event with great enthusiasm and enjoyment. The program aims to make students come under one roof, showcase their extracurricular skills, showcase their dancing skill and convey some effective message to society through their creativity and cherish their memories for life time.



Program was concluded by distribution of prizes among winners as Mr. Fresher's and Ms. Fresher's. The event not only celebrated the conclusion of an academic chapter but also symbolized the beginning of new journeys filled with dreams and aspirations. It strengthened the bond between students and faculty, creating lifelong memories to cherish.



Department of Electronics and Telecommunication

"Blood Donation Camp 2024-25"

Resource Person: Ayush Blood Centre & Components Lab, Dhantoli, Nagpur.



Blood Donation Camp was conducted to let the students understand and raise awareness of the importance of blood donation which helps to relieve insufficient blood supply. "A Drop of Blood, can save a Life". The blood donation campaign received an overwhelming response from students as well as faculties. During the campaign proper hygiene was maintained and the blood donors were offered with refreshments. "The department decided to promote and support the establishment of effective Blood donation programs in future also.

3 Days Workshop on Hands on training on assembling the Verilog Module on Altera platform using Quartus II



The 3-day workshop on "Hands-on Training on Assembling the Verilog Module on Altera Platform Using Quartus II" aims to provide participants with practical skills in digital circuit design using Verilog and the Quartus II software.

Over the course of the workshop, participants will:

Learn Verilog: Understand the syntax and structure of Verilog for creating digital components.

Explore Quartus II: Gain hands-on experience with Quartus II for designing, simulating, and synthesizing Verilog code for Altera FPGAs.



FPGA Implementation: Learn to program and test designs on Altera FPGA boards, including constraint assignment and optimization for timing and resources.

By the end of the workshop, participants will be able to design, simulate, and implement digital systems on FPGAs, providing a strong foundation for further FPGA development and digital hardware design.

Department of Electronics and Telecommunication

Teachers Day Celebration-2k24



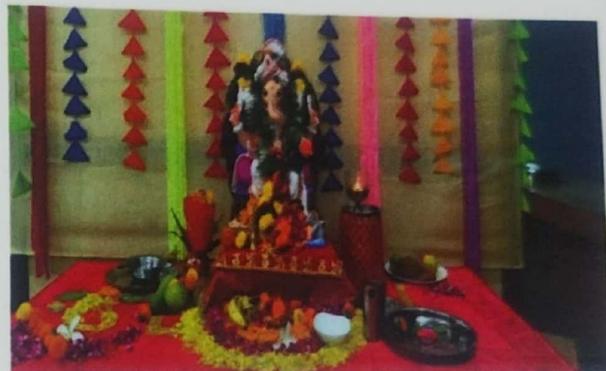
Teachers' Day celebration was a resounding success which was done by the incredible efforts of students. The variety of games and thoughtful gifts to faculties truly showcased students' appreciation and creativity, where teachers & students participated in the event with great enthusiasm and enjoyment. This celebration was a testament to the strong bond between students and teachers. The enthusiasm and dedication displayed by the students in organizing such a memorable event are commendable.



Teachers' Day celebration was a resounding success which was done by the incredible efforts of students. The variety of games and thoughtful gifts to faculties truly showcased students' appreciation and creativity, where teachers & students participated in the event with great enthusiasm and enjoyment. This celebration

was a testament to the strong bond between students and teachers. The enthusiasm and dedication displayed by the students in organizing such a memorable event are commendable.

Ganesh Puja Ceremony-2k24.



The Ganesh Puja ceremony at college is a vibrant and joyous event that brings together students and faculty to celebrate the auspicious occasion. The purpose of this ceremony is to seek the blessings of Lord Ganesha, the remover of obstacles, for a successful academic year and personal growth. It fosters a sense of community, cultural appreciation, and spiritual well-being among all participants. Through prayers, rituals, and cultural performances, the ceremony aims to instill values of unity, respect, and positivity within the college environment.



Department of Electronics and Telecommunication

The Ganesh Puja ceremony at college was a truly memorable event, uniting students and faculty in a celebration of tradition and community. The ceremony was marked by vibrant rituals, heartfelt prayers, and captivating cultural performances, all of which highlighted the rich cultural heritage we share. This event not only allowed us to seek the blessings of Lord Ganesha for a prosperous academic year but also reinforced our commitment to fostering an inclusive and respectful environment. As we move forward, let us carry the spirit of unity and positivity from this celebration into all our future endeavors, continuing to support and uplift one another.

5 Day Workshop on Antenna Design, Simulation and Fabrication Aspects

Resources used: CST software

Brief Description of Activity: The students were well acquainted with the fundamentals of Antenna, its design parameters, concept of Microstrip patch Antenna, its various design parameters etc. The students were also got exposure of CST software. They have designed the Microstrip patch antenna in the frequency range of 2.2 GHz, 4 GHz & 5 GHz. The students were enjoyed the hand on session lot and were benifited with the antenna design exposure. The visit to Antenna design Laboratory of E & T Department, YCCE, Nagpur was also organised. In this visit session students were well acquainted with fabrication aspects and testing of antenna on Network analyser. The students were benifitted and enjoyed lot this overall workshop with full enthusiasm. This is organised by R&D cell and TELE-ERA students forum



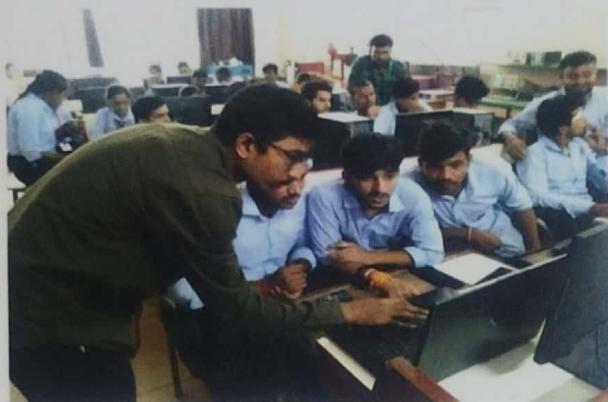
One day workshop on Arduino

The Department of Electronics & Telecommunication at Priyadarshini College of Engineering organized a one-day **Arduino workshop** on 18th September 2024, led by alumni Mr. Kshitij Madankar and Mr. Nishant Bhoj (Batch of 2023). The session was designed to introduce students to the fundamentals of Arduino and its real-time applications in mini-projects. With 33 students actively participating, the workshop combined theoretical concepts with practical implementation, encouraging students to ideate, prototype, and innovate. The hands-on experience equipped students with essential skills to build microcontroller-based systems with confidence and creativity.

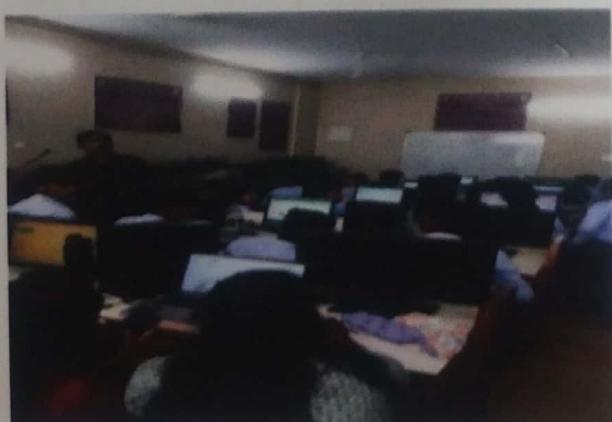


Department of Electronics and Telecommunication

One Day Workshop On Raspberry Pi



The Department of Electronics & Telecommunication at Priyadarshini College of Engineering organized a one-day **Raspberry Pi** workshop on 21st September 2024, a comprehensive Raspberry Pi workshop was conducted by Dr. V. V. Panchbhai. Attended by 24 students, the session focused on introducing the architecture, functionalities, and project-based applications of Raspberry Pi in embedded systems. Students gained clarity on its usage in various electronics-based mini-projects, enriching their technical repertoire and motivating them to explore advanced computing solutions. The interactive approach made complex topics accessible and applicable, further strengthening students' practical understanding.



One Day Workshop on C++

C++ Programming Workshop: Bridging Logic with Language

The Department of Electronics & Telecommunication Engineering at Priyadarshini College of Engineering continues to emphasize the importance of technical upskilling through practical learning platforms. As part of this endeavor, a two-day workshop on **C++ Programming** was successfully organized under the Field Project Lab initiative and the department's technical club *Tele-Era*, from 24th to 25th September 2024 at the SPDP Lab, Aeronautical Building. A total of 65 enthusiastic **third-semester students** actively participated in this engaging hands-on session led by **Mr. Suyash Yelmule**, a proud alumnus of the department.



The primary objective of the workshop was to instill foundational programming knowledge in **C++**, a versatile and widely-used object-oriented language crucial for logic building and software development. Mr. Yelmule guided students through the syntax, structure, and practical implementation of C++ using the **Programiz simulator**, ensuring real-time application of concepts. The workshop covered essential programming modules, including variable handling, control structures, object-oriented features, and function-based programming.

Department of Electronics and Telecommunication