



PRIYADARSHINI COLLEGE OF ENGINEERING

(Recognised by A.I.C.T.E., New Delhi & Govt. of Maharashtra, Affiliated to R.T.M.Nagpur University)
Near CRPF Campus, Hingna Road, Nagpur-440 019, Maharashtra (India)
Phone : 07104 - 236381, 237307, Fax : 07104 - 237681,



One Page Report of Students Activities

Name of the Dept:-Electronics and Communication Engineering.

Date:-23rd September,2024.

Title:-"VHDL Coding-Simulation and Synthesis" (One Day Workshop) .

Venue:-Lab-1,404,Electronics and Communication Department,Aeronautical Building.

Teacher Coordinator:- Dr(Mrs)Gouri P.Halde,Prof. G.G.Sarmokaddam

Resource Person: Prof.Sharda Mungale

Students Involved:-27.

Description:-

Electronics and Communication Department under IETE student Forum has organized a One Day Workshop on 23rd September, 2024 at 10:30 AM, in Lab-1,404, Electronics and Communication Department. The workshop was organised with an objective to give practical experience for VHDL coding to Vth sem students and for those who have taken this subject as program Elective as well as other interested students. Dr(Mrs) A.P.Rathkanthiwar , HOD, Electronics & Communication Engg. Enlightened the students with her speech. Also Prof.V.V.Dabhade , IETE-ISF Faculty Incharge appreciated the students' efforts and motivated them for grabbing opportunities in VLSI sector. At last vote of thanks was given by Dr(Mrs) Gouri P.Halde. The workshop was a great success and all the faculties and students developed interest in the VHDL coding, simulation and synthesis.

Outcomes:-1) Students got the hands on and practical exposure for VHDL coding and simulation..

2) They gain the comprehensive understanding of VHDL coding Simulation on Xilinx ISE and Synthesis fundamentals.

3) Got insight about digital design using hardware description language.

Relievement to PO,PSO's: PO1,PO2,PO3, PO5,PSO2



Dr(Mrs)G.P.Halde

Prof.G.G.Sarmokaddam

Coordinators

Dr(Mrs)A.P.Rathkanthi

HOD, Elect. & Commn.Engg